What is Claimed is:

1. A comparator unit comprising:

a first comparator responsive to a first signal group, the first comparator determining when a first predetermined relation is present to a first reference signal group;

a second comparator responsive to a second signal group, the second comparator determining when a second predetermined relation is present to a second reference signal group; and

a second inter-comparator conductor, the second inter-comparator conductor applying an indicia of an identification of the second predetermined condition to first comparator, the first comparator generating an event signal when the first and the second predetermined conditions are identified.

2. The comparator unit as recited in claim 1 wherein the first and the second signal groups are the same.

3. The comparator unit as recited in claim 1 wherein the first and second signal groups are address signal groups.

4. The comparator unit as recited in claim 1 wherein the first and the second signal groups are same address signal group.

5. The comparator unit as recited in claim 1 wherein either one of the first and the second comparator can generate an event signal when at least one of a touching requirement and an exact requirement is satisfied by an applied address signal group.

6. A comparator unit comprising:

a first comparator and a second comparator, each comparator including:

a comparison logic unit for comparing an input signal group with a predetermined condition is identified; and

an event signal generating unit, the comparison logic unit applying a signal to the event generator unit and to the event signal generating unit of the other comparator when the predetermined condition is identified, the event generating unit generating an event signal when the signals from the two comparator logics have predetermined values.

7. The comparator unit as recited in claim 6 wherein each comparator includes a data qualifying unit, the data qualifying unit responsive to an input signal, the input signal determining when a preestablished signal group has certain characteristics, the data qualifying unit applying

a control signal to the comparison logic unit determining whether generation of an event signal is enabled.

8. The comparator unit as recited in claim 6 wherein the input signal groups are address signal groups, the predetermined conditions each reference an address signal group.

9. The comparator unit as recited in claim 8 wherein the address signal groups are the same signal group.

10. The comparator unit as recited in claim 6 wherein the predetermined conditions are entered in the comparator logic by control signals.

11. The comparator as recited in claim 10 wherein each comparator can operate independently, each comparator capable of generating an event signal in response to at least one of a touching requirement and an exact requirement.

12. The method of determining when a first and a second input signal group meets at least one predetermined condition, the method comprising:

determining in a first comparator when the first input signal group meets a first predetermined condition;

determining in a second comparator when the second input signal group meets a second predetermined condition; and

generating an output signal when the first and the second predetermined conditions are met.

13. The method as recited in claim 12 wherein the first and the second input signal group are different address signal groups.

14. The method as recited in claim 12 wherein the first and the second input signal groups are the same address signal group.

15. The method as recited in claim 12 wherein the at least one predetermined condition is selected from the group consisting of a touching requirement and an exact requirement.

16. The method as recited in claim 12 further comprising applying a signal to the comparators indicative an associated signal group characteristic, the signal controlling generation of the output signal.

17. In a target processor, apparatus for generating a trigger signal, the apparatus comprising:

a plurality of event signal generating units, wherein at least one signal generating unit is a comparator unit, the comparator unit including:

a first comparator and a second comparator, each comparator having:

a comparison logic unit for comparing an input signal group with a predetermined condition is identified; and

an event signal generating unit, the comparison logic unit applying a signal to the event generator unit and to the event signal generating unit of the other comparator when the predetermined condition is identified, the event generating unit generating an event signal when the signals from the two comparator logics have predetermined values.

a trigger generation unit coupled to the plurality of event signal generation units, the trigger generation unit responsive to at least one preselected event signal for generating an associated trigger signal, the trigger generating unit generating a trigger control signal;

18. The target processor as recited in claim 17 wherein the comparator unit receives a program counter address input signal.

19. The target processor as recited in claim 17 wherein one comparator receives a program counter address counter address input signal and the second comparator

received an address signal group referenced the program counter address.

20. The target processor as recited in claim 17 wherein the preselected condition is selected from the group consisting of a touching requirement and an exact requirement.